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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/666,169	09/19/2003	Abhijit Jas	884.911US1	7098
21186	7590	04/27/2006	EXAMINER	
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. BOX 2938 MINNEAPOLIS, MN 55402			DILDINE JR, R STEPHEN	
		ART UNIT	PAPER NUMBER	
		2133		

DATE MAILED: 04/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/666,169	JAS ET AL.
	Examiner	Art Unit
	R. Stephen Dildine	2133

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1,5,8,10,15,18,20,22 and 28 is/are rejected.
- 7) Claim(s) 2-4,6,7,9,11-14,16,17,19,21,23-27 and 30 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 19 September 2003 is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 3/26/04 & 9/19/03.
- 4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: _____.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 5, 8, 10, 15, 18, 20 and 22 are rejected under 35 U.S.C. 102(B) as being anticipated by

Hiraide *et al.* (2002/0124217) who teaches a seed register ([0147] “A plurality of seed values (seed value set) to be set in the LFSR 2 are beforehand prepared”, where it is clear to one of ordinary skill in the art at the time of applicants’ invention that there must be a storage means for the preprepared seed value), a LFSR (LFSR 2 in Fig. 3), a signature register (MISR 7 in Fig. 3) providing a compressed response (space compactor 6 in Fig. 3), plural scan chains (shown in Fig. 3 as scan path#0 through scan path#n-1), and masking logic for masking the input to a MISR(5 in Fig. 3).

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 28-29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hiraide *et al.* (2002/0124217) as applied to claim 22 above, and further in view of Wuu *et al.* (2005/0050400) who teaches one of ordinary skill in the art at the time of applicants’ invention to test computer memory devices (“A computer-implemented method for use in a computer system including a memory”, claim 1. [0061] “In particular, the system 500 includes a memory test engine 504.”) with scan chains ([0135] “Alternatively, for example, the latch 808 may be scannable, in which case the bit SRP[i] may be serially shifted into the latch 808 (and the other latches, not shown, in the system 500) using a scan chain”) which would be used in the combination of parent claim 22 because, as stated in paragraph [0039] of Wuu *et al.* “Increasing the number of bits required to encode a shift redundancy pattern can be problematic because a larger pattern typically requires a larger number of serial shift elements (e.g., latches) to implement, thereby increasing the size and cost of the required circuitry, and because it takes more time to transmit a longer pattern during system startup and therefore to perform memory repair.”

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Claims 2-4, 6-7, 9, 11-14, 16-17, 19, 21, 23-27 and 30 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Kapur *et al.* and Wohl *et al.* are cited to show teachings similar to that of Hiraide *et al.*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to R. Stephen Dildine whose telephone number is (571) 272-3820. The examiner can normally be reached on M - F 5:30 am to 2:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

R. Stephen Dildine
Primary Examiner
Art Unit 2133



R. Stephen Dildine